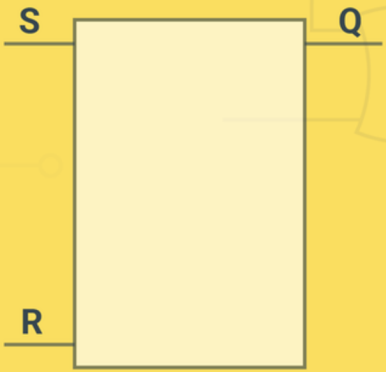
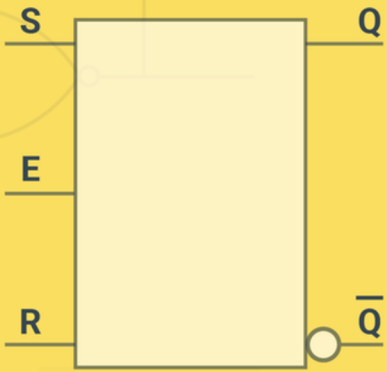
CIRCUIT DIGEST

SR Latch

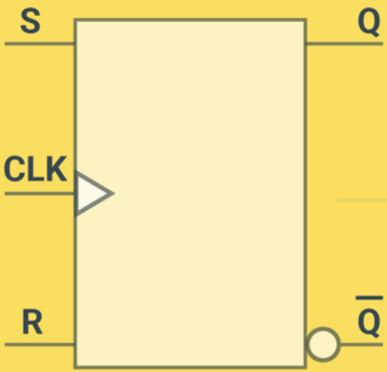
Types, Truth Tables & Real-World Simulation



SR-Latch



Enabled SR-Latch

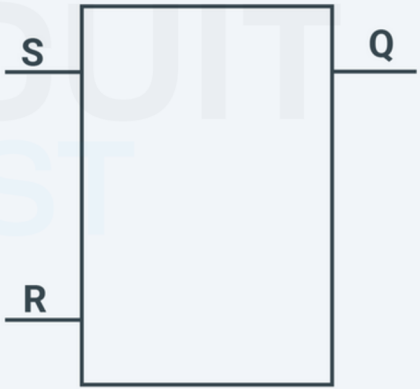


Clocked SR-Latch

SR Latch

Truth Table

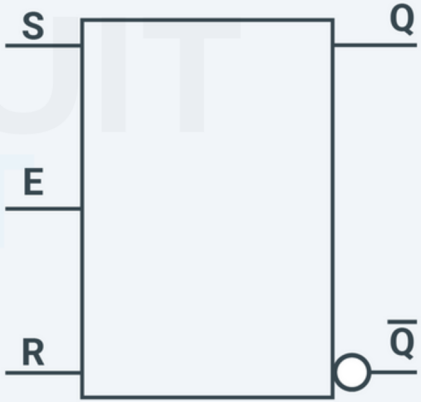
Input		Output
Set	**Reset**	**Q**
Low	Low	Last State
Low	High	Low
High	Low	High
High	High	Unpredictable
High or Low		Last State



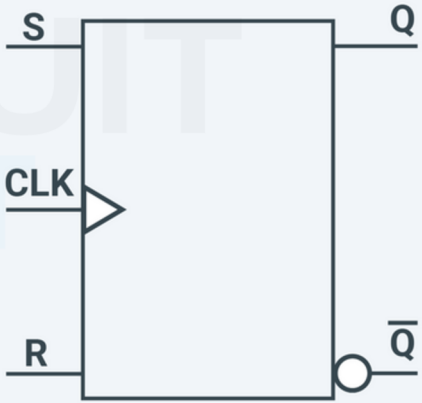
Gated SR Latch

Truth Table

Input			Output	
Set	**Reset**	**Enable**	**Q**	**$\overline{Q}$**
Low	Low	High	Last State	
Low	High		Low	High
High	Low		High	Low
High	High		Unpredictable	
High or Low		Low	Last State	



Clocked SR Latch

Truth Table

Input			Output	
Set	**Reset**	**Clock**	**Q**	**$\overline{Q}$**
Low	Low	Raising or Falling Edge	Last State	
Low	High		Low	High
High	Low		High	Low
High	High		Unpredictable	
High or Low		Stable High or Low	Last State